

In the claims:

Please amend the claims as indicated.

- B:
- 1 1. (Previously amended) A system comprising:
2 a battery;
3 a super-capacitor (SC) coupled in parallel to the battery;
4 a computer system coupled to the battery and the SC; and
5 a current limiter, coupled to the battery, the SC and the computer system, to
6 prevent excess current from flowing from the battery to the SC.
 - 1 2. (Original) The system of claim 1 wherein the current limiter prevents excess
2 current from flowing from the SC to the battery.
 - 1 3. (Previously amended) The system of claim 1 wherein the SC prevents transients
2 from the computer system from affecting the battery voltage.
 - 1 4. (Previously amended) The system of claim 3 wherein the SC has a capacitance of
2 20 farad and a resistance of 5 m .
 - 1 5. (Original) The system of claim 1 wherein the computer system comprises:
2 a power delivery subsection; and
3 a plurality of hardware components coupled to the power delivery subsection.
 - 1 6. (Original) The system of claim 5 wherein the power delivery subsection
2 comprises:
3 a system voltage regulator;
4 a chipset voltage regulator; and

B₁
with

5 a central processing unit (CPU) voltage regulator.

1 7. (Original) The system of claim 2 wherein the current limiter comprises:

2 a first transistor coupled to the battery;

3 a second transistor coupled to the first transistor; and

4 a resistor coupled to the second transistor, the SC and the computer system.

1 8. (Original) The system of claim 7 wherein the current limiter further

2 comprises:

3 a first comparator with inputs coupled across the resistor and an output coupled to
4 the gate of the second transistor; and

5 a second comparator with inputs coupled across the resistor and an output coupled
6 to the gate of the first transistor.

1 9. (Original) The system of claim 8 wherein the first comparator deactivates the
2 second transistor if the voltage across the resistor is greater than a first predetermined
3 threshold.

1 10. (Original) The system of claim 9 wherein the second comparator deactivates
2 the first transistor if the voltage across the resistor is greater than a second predetermined
3 threshold.

1 11. (Previously amended) A system comprising:

2 a battery;

3 a super-capacitor (SC) coupled in parallel to the battery;

4 a power delivery system coupled to the battery and the SC; and
5 a current limiter, coupled to the battery, the SC and the power delivery system, to
6 prevent excess current from flowing from the battery to the SC.

1 12. (Original) The system of claim 11 wherein the current limiter prevents excess
2 current from flowing from the SC to the battery.

1 13. (Previously amended) The system of claim 11 wherein the SC prevents transients
2 from the computer system from affecting the battery voltage.

1 14. (Original) The system of claim 11 wherein the power delivery system
2 comprises:

3 a first voltage regulator; and

4 a second voltage regulator.

1 15. (Currently amended) A current limiter comprising:

2 a first transistor coupled to a battery;

3 a second transistor coupled to the first transistor; and

4 a resistor coupled to the second transistor, and a super-capacitor (SC);

5 wherein the [current limiter] second transistor prevents excess current from

6 flowing from the battery to the SC whenever the second transistor is deactivated.

1 16. (Original) The current limiter of claim 15 further comprising:

2 a first comparator with inputs coupled across the resistor and an output coupled to

3 the gate of the second transistor; and

R₁
cont 4 a second comparator with inputs coupled across the resistor and an output coupled
5 to the gate of the first transistor.

1 17. (Original) The current limiter of claim 16 wherein the first comparator
2 deactivates the second transistor if the voltage across the resistor is greater than a first
3 predetermined threshold.

1 18. (Original) The current limiter of claim 17 wherein the second comparator
2 deactivates the first transistor if the voltage across the resistor is greater than a second
3 predetermined threshold.
